

ANNAI J.K.K.SAMPOORANIAMMAL POLYTECHNIC COLLEGE

T.N.PALAYAM, GOBI(TK), ERODE(DT) - 638506

STAFF PROFILE

1. PERSONAL DETAILS

NAME : S.SRINIVASAN
FATHER'S NAME : T.M .SENGOTTAIYAN
DATE OF BIRTH : 07-05-1976
SEX : MALE
NATIONALITY : INDIAN
BLOOD GROUP : AB+
MARITAL STATUS : UNMARRIED
LANGUAGES : TAMIL, ENGLISH
EXPERIENCE : Jun 2000 to Jun 2007
ADDRESS : 66 B, A.K. PALANIYAPPA STREET , THAVITTUPALAYAM,
ANTHIYUR (POST)

2. EDUCATIONAL QUALIFICATIONS:

DEGREE / COURSE	INSTITUTION	UNIVERSITY	YEAR OF PASSING
M.E (EEE)	GOVERNMENT COLLEGE OF TECHNOLOGY	ANNA UNIVERSITY, COIMBATORE.	2009
B.E(EEE)	KONGU ENGINEERING COLLEGE	BHARATHIAR UNIVERSITY	1999

3. TECHNICAL SKILLS: MATLAB (simulink-simpowersystem),

4. PROJECT EXPERIENCES:

PROJECT NAME: REALIZATION OF SOFT COMMUTATION WITH ZERO CURRENT TRANSITION IN FULL BRIDGE DC/DC CONVERTER

DURATION: 5 Months (August 2008 to December 2008)

TEAM SIZE:

ENVIRONMENT: Software: Mat lab 7

RESPONSIBILITIES:

PROJECT DESCRIPTION:

ANNAI J.K.K.SAMPOORANIAMMAL POLYTECHNIC COLLEGE

T.N.PALAYAM, GOBI(TK), ERODE(DT) - 638506

PROJECT DESCRIPTION:

This thesis describes a zero current transition pulse width modulation control of a full bridge dc-dc converter. In this proposed method, zero current switching is adopted for main IGPT switches and auxiliary switch in the entire load ranges. In addition to this, soft commutations for the output rectifier diodes are also realized. The auxiliary Circuit is used to turn on the main switches softly. The entire circuit is simulated in the matlab-simulink environment. From the simulation results, it has been shown that the proposed method achieves ZCS for all active switches and eliminates the diode reverse recovery problem

5. AREA OF INTEREST Electrical Machines, Power Electronics, Digital Electronics

6. EXTRA & CO-CURRICULAR ACTIVITY: Advisor for Institution of Engineers in INDIA (**IEI**)

Member

7. PRESENTATIONS:

Name: National conference on computing, control and communication systems.

Place: Hindustan College of Engineering and Technology, Coimbatore.

Name: Mobile networking and Embedded Systems

Organizer: IEEE students branch GCT, Coimbatore.

Name: Embedded System Design using AMR and MICRO C/OS II

Organizer: VI Microsystems.

Name: Graphical system design using LABVIEW.

Venue: GCT, Coimbatore.

8. OTHER EXPERIENCES: ONE AND HALF YEAR EXPERIENCED IN INDUSTRIES